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The all-digital transmitter consists of one QAM modulator and counter using the high speed reference clock. However, because ...

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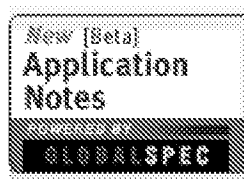
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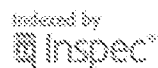
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...used. The output frequency range coverage...up to the high speed reference clock frequency...high Power QAM Modulator"[0010] U...Direct QAM Modulator" (SUMMARY...0013] a high speed reference clock providing...the desired output frequency; [0014] two...
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...accumulator. The output frequency range coverage...up to the high speed reference clock frequency...a desired output frequency comprising: [0011]a high speed reference clock providing...the desired output frequency; [0012] programmable...
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Class/Subclass:

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Status: 41 /NON FINAL ACTION MAILED

Status Date: 11/27/2007

Confirmation Number: 9044

Oral Hearing: NO

Title of Invention: METHOD AND APPARATUS FOR DIGITAL VECTOR QAM
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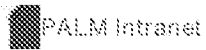
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Inventor Information for 10/796416

| Inventor Name | City | State/Country |
|-----------------|--------------|---------------|
| HARRON, GERALD | MARTENSVILLE | CANADA |
| KUMAR, SURINDER | VICTORIA | CANADA |

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| 10796418 | 7012477 | 150 | 03/10/2004 | MODULATION USING DISCRETE AMPLITUDE ADJUSTMENT AND DUAL DIGITAL DELAY LINES | HARRON, GERALD |
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| 60414393 | Not Issued | 159 | 09/30/2002 | Method and apparatus for reducing the power consumption of the power amplifier used in a QAM modulator | HARRON, GERALD |
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| | Issued | | | fractional RF signal synthesis with phase modulation | |
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| 06216407 | Not Issued | 161 | 12/15/1980 | LOW-CHOLESTEROL SAUSAGE-LINK ANALOG AND PROCESS THEREFOR | KUMAR, SURINDER |
| 06288091 | 4376134 | 250 | 07/28/1981 | LOW-CHOLESTEROL SAUSAGE ANALOG AND PROCESS THEREFOR | KUMAR, SURINDER |
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| 08837395 | 5852389 | 150 | 04/17/1997 | DIRECT QAM MODULATOR | KUMAR, SURINDER |
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